

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Reducing Number of Pins Required to Test Integrated Circuits						
<p>Application Number :</p> <p>Confirmation Number:</p> <p>First Named Applicant: Arindam SAHA</p> <p>Attorney Docket Number: TI-36666</p> <p>Art Unit:</p> <p>Examiner:</p> <p>Search string: (10302886).pn</p>							
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
SS	1	10302886	2002-11-25	Gupte et al			
Signature							
Examiner Name				Date			
A. J. SAHA SAHIB SINGH				02/18/06			